2.5 TRIGGER

CHARACTERISTICS	SPECIFICATIONS	ADDITIONAL INFORMATION
* Sources		Selected independently.
Channel A Signal	CHAN A	
Channel B Signal	CHAN B	
External Trigger Input	EXT	
* External Trigger Input **Connector**	Dual Safety Banana Jack	**External Trigger Input common (low) jack is electrically connected to the Channel A and Channel B commons (outer contact of BNC's).**
* External Trigger Input Impedance		
R parallel	1 MΩ ± 1%	If used for mV DC > 1 MΩ.
C parallel	25 pF	Including Banana to BNC adapter.
* Trigger Error		For frequencies < 1 MHz.
Voltage Level	± 1 LSB ± 0.5 div	5s/div...50 µs/div. 20 µs/div...10 ns/div.
Time Delay	± 1 LSB ± 5 ns	
* Maximum External Trigger Input (rms)	300 V	Frequency dependent, see fig. 2.2.
* Trigger Sensitivity		For Models 95 and 97, values must be multiplied by 5 in 2 mV/div. and 1 mV/div.
Channel A or B		
@ 100 MHz	≤ 4 div	
@ 60 MHz	≤ 1.5 div	
@ 10 MHz	≤ 0.8 div	
External Trigger Input		TTL logic compatible using 10:1 attenuation Probe.
* Trigger Slope Selection	positive going negative going	
* Trigger Level Control Range		
Channel A or B	± 4 div	
Trigger at 50%	0.5 x peak/peak value	Measured during 20 ms.
External Trigger Input	Fixed @ TTL:10	Switchable to TTL via set-up menu.